

2123

PTO/SB/21 (09-04)

Approved for use through 07/31/2006. OMB 0651-0031

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**TRANSMITTAL
FORM**

(to be used for all correspondence after initial filing)

Total Number of Pages in This Submission

Application Number

10/066,051

Filing Date

January 31, 2002

First Named Inventor

Li-Shiuan PEH

Art Unit

2123

Examiner Name

Kevin J. TESKA

Attorney Docket Number

100110288-1

ENCLOSURES (Check all that apply)

<input type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Drawing(s)	<input type="checkbox"/> After Allowance Communication to TC
<input type="checkbox"/> Fee Attached	<input type="checkbox"/> Licensing-related Papers	<input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences
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<input type="checkbox"/> Affidavits/declaration(s)	<input type="checkbox"/> Power of Attorney, Revocation	<input type="checkbox"/> Status Letter
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<input checked="" type="checkbox"/> Information Disclosure Statement	<input type="checkbox"/> Request for Refund	24 Cited References
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<input type="checkbox"/> Reply to Missing Parts/Incomplete Application	<input type="checkbox"/> Landscape Table on CD	
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Firm Name	Law Offices of Derek J. Westberg		
Signature			
Printed name	Derek J. WESTBERG		
Date	May 10, 2005	Reg. No.	40,872

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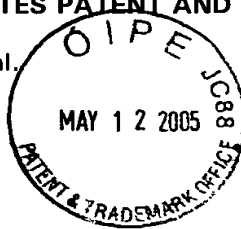
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IN THE
UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Li-Shiuan PEH et al.
Application No.: 10/066,051
Filing Date: 01-31-2002
Title: Generating Interconnect Fabric Requirements



Confirmation No.: 3926
Examiner: Kevin J. TESKA
Group Art Unit: 2123

Mail Stop Amendment
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

This Information Disclosure Statement is submitted:

- ☒ under 37 CFR 1.97(b), or
(Within three months of filing national application; or date of entry of national application; or before mailing date of first office action on the merits; whichever occurs last)
- ☐ under 37 CFR 1.97 (c) together with either a:
 - ☐ Statement under 37 CFR 1.97(e), or
 - ☐ a \$180.00 fee under 37 CFR 1.17(p), or
(After the CFR 1.97 (b) time period, but before final action or notice of allowance, whichever occurs first)
- ☐ under 37 CFR 1.97 (d) together with a:
 - ☐ Statement under 37 CFR 1.97(e)(1) or (2), and
 - ☐ a \$180.00 fee set forth in 37 CFR 1.17(p).
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Please charge to Deposit Account **08-2025** the sum of \$0.00. At any time during the pendency of this application, please charge any fees required or credit any overpayment to Deposit Account **08-2025** pursuant to 37 CFR 1.25.

☒ Applicant(s) submit herewith Form PTO 1449 - Information Disclosure Statement together with any required copies of patents, publications or other information of which applicant(s) are aware, which applicant(s) believe(s) may be material to the examination of this application and for which there may be a duty to disclose in accordance with 37 CFR 1.56.


☐ A concise explanation of the relevance of foreign language patents, foreign language publications and other foreign language information listed on PTO Form 1449, as presently understood by the individual(s) designated in 37 CFR 1.56 (c) most knowledgeable about the content is given on the attached sheet, or where a foreign language patent is cited in a search report or other action by a foreign patent office in a counterpart foreign application, an English language version of the search report or action which indicates the degree of relevance found by the foreign office is listed on form PTO 1449 and is enclosed herewith.

It is requested that the information disclosed herein be made of record in this application.

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Respectfully submitted,

Li-Shiuan PEH et al.

By 

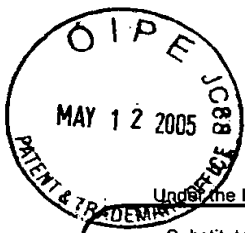
Derek J. WESTBERG

Attorney/Agent for Applicant(s)

Reg. No. **40,872**

Date: **May 10, 2005**

Telephone No.: **(408) 293-9000**



PTO/SB/08A (08-03)

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Substitute for form 1449/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Complete if Known

Application Number	10/066,051
Filing Date	January 31, 2002
First Named Inventor	Li-Shiuan PEH
Art Unit	2123
Examiner Name	Kevin J. TESKA
Attorney Docket Number	100110288-1

Sheet 1 of 6

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
	1A	US- 5,634,011-A	05-27-1997	Auerbach et al.	
	1B	US- 5,634,004-A	05-27-1997	Gopinath et al.	
	1C	US- 5,107,489-A	04-21-1992	Brown et al.	
	1D	US- 5,970,232-A	10-19-1999	Passint et al.	
	1E	US- 6,628,649-B1	09-30-2003	Raj et al.	
	1F	US- 6,614,796-B1	09-02-2003	Black et al.	
	1G	US- 5,831,996-A	11-03-1998	Abramovici et al.	
	1H	US- 5,878,232-A	03-02-1999	Peram Marimuthu	
	1I	US- 6,195,355-B1	02-27-2001	Noritoshi Demizu	
	1J	US- 6,701,327-B1	03-02-2004	Jones et al.	
	1K	US- 5,835,498-A	11-10-1998	Kim et al.	
	1L	US- 4,920,487-A	04-24-1990	Paul T. Baffes	
	1M	US- 5,329,619-A	07-12-1994	Pagé et al.	
	1N	US- 5,113,496-A	05-12-1992	McCalley et al.	
	1O	US- 6,594,701-B1	07-15-2003	Alessandro Forin	
	1P	US- 6,557,169-B1	04-29-2003	Bernard Erpeldinger	
	1Q	US- 6,766,381-B1	07-20-2004	Barker et al.	
	1R	US- 6,253,339-B1	06-26-2001	Tse et al.	
	1S	US- 6,701,327-B1	03-02-2004	Jones et al.	

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T ⁶
		Country Code ³ *Number ⁴ *Kind Code ⁵ (if known)				
	1T	WO-96/17458-A	06-06-1996	British Telecommunications Public Limited Company		

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Application Number	10/066,051
Filing Date	January 31, 2002
First Named Inventor	Li-Shiuan PEH
Art Unit	2123
Examiner Name	Kevin J. TESKA
Attorney Docket Number	100110288-1

Sheet 2 of 6**U. S. PATENT DOCUMENTS**

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		Number-Kind Code ² (if known)			
	2A	US- 5,524,212-A	06-04-1996	Somani et al.	
	2B	US- 6,603,769-B1	08-05-2003	Thubert et al.	
	2C	US- 6,757,731-B1	06-29-2004	Barnes et al.	
	2D	US- 6,148,000-A	11-14-2000	Feldman et al.	
	2E	US- 6,611,872-B1	08-26-2003	Steven McCanne	
	2F	US- 5,857,180-A	01-05-1999	Hallmark et al.	
	2G	US- 5,307,449-A	04-26-1994	Keiley et al.	
	2H	US- 5,651,005-A	07-22-1997	Kwok et al.	
	2I	US- 6,141,355-A	10-31-2000	Palmer et al.	
	2J	US- 5,838,919-A	11-17-1998	Schwaller et al.	
	2K	US- 6,038,219-A	03-14-2000	Mawhinney et al.	
	2L	US- 6,668,308-B2	12-23-2003	Barroso et al.	
	2M	US- 6,570,850-B1	05-27-2003	Gutierrez et al.	
	2N	US- 6,363,334-B1	03-26-2002	Andrews et al.	
	2O	US- 6,108,782-A	08-22-2000	Fletcher et al.	
	2P	US- 6,539,531-B2	03-25-2003	Miller et al.	
	2Q	US- 5,138,657-A	08-11-1992	Colton et al.	
	2R	US- 6,331,905-B1	12-18-2001	Ellinas et al.	
	2S	US- 6,003,037-A	12-14-1999	Kassabgi et al.	

FOREIGN PATENT DOCUMENTS

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Sheet 3 of 6**U. S. PATENT DOCUMENTS**

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		Number-Kind Code ² (if known)			
	3A	US- 5,245,609-A	09-14-1993	Ofek et al.	
	3B	US- 5,805,578-A	09-08-1993	Stirpe et al.	
	3C	US- 6,047,199-A	04-04-2000	DeMarco	
	3D	US- 5,581,689-A	12-03-1996	Slominski et al.	
	3E	US- 5,815,402-A	09-29-1998	Taylor et al.	
	3F	US- 6,157,645-A	12-05-2000	Shobatake	
	3G	US- 6,212,568-B1	04-03-2001	Miller et al.	
	3H	US- 2003-0145294 A1	07-31-2003	Ward et al.	
	3I	US- 2003-0065758 A1	04-03-2003	O'Sullivan et al.	
	3J	US- 2002-0083159 A1	06-27-2002	Ward et al.	
	3K	US- 6,031,984-A	02-29-2000	Walser	
	3L	US- 6,526,420-B2	02-25-2003	Borowsky et al.	
	3M	US- 2005-0021583-A1	01-17-2005	Andrzejak et al.	
	3N	US- 6,687,222-B1	02-03-2004	Albert et al.	
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		Filing Date	January 31, 2002		
		First Named Inventor	Li-Shiuan PEH		
		Art Unit	2123		
		Examiner Name	Kevin J. TESKA		
Sheet	4	of	6	Attorney Docket Number	100110288-1

NON PATENT LITERATURE DOCUMENTS			
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	4A	Mathew ANDREWS et al., Integrated Scheduling of Unicast and Multicast Traffic in an Input-Queued Switch, 1999. < http://cm.bell-labs.com/cm/ms/who/andrews/infocom99switch.ps >	
	4B	Viraphol CHAIYAKUL, Assignment Decision Diagram for High-Level Synthesis, 1992.	
	4C	Andre DEHON, Notes on Coupling Processors with Reconfigurable Logic, MIT Transit Project, Transit Note #118, 1995.	
	4D	Cathy FULTON et al., Impact Analysis of Packet-Level Scheduling On an ATM Shared-Memory Switch, Infocom, Volume 3 pp. 947-954, 1998, IEEE, New York, NY.	
	4E	Hiroshi INOSE, An Introduction to Digital Integrated Communication Systems, pages 87-89, 1979, Peter Peregrinus Ltd., Stevenage, United Kingdom.	
	4F	A. Richard NEWTON, Interface-Based Design; Introduction, University of California at Berkeley, April 1999.	
	4G	Derek C. W. PAO, A Congestion Control Algorithm for Multipoint-to-Multipoint ABR Service in ATM Network, Proceedings of the IEEE Conference on High Performance Switching and Routing, pp. 167-175, June 26, 2000, IEEE Press, New York, NY.	
	4H	Rainer SCHOENEN et al., Weighted Arbitration Algorithms with Priorities for Input-Queued Switches with 100% Throughput, 1999, < http://www.iss.rwth-aachen.de/Projekte/Theo/papers/Schoenen99bssw.ps.gz >	
	4I	Oryal TANIR et al., Structural Reuse In The Design Of ATM Switch Fabrics, 1997.	
	4J	I. WIDJAJA et al., Performance Issues in VC-Merge Capable ATM LSRs, RFC 2682, September 1999, The Internet Society, Reston, VA.	

Examiner Signature		Date Considered	
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		Examiner Name	Kevin J. TESKA
		Attorney Docket Number	100110288-1
Sheet	5	of	6

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	5A	Julie WARD, US Patent Application No.: 09/707,227, Filing Date: November 6, 2000.	
	5B	Julie WARD et al., Storage Area Network (SAN) Fabric Design, PowerPoint presentation given at Berkeley, IEOR Department Seminar, Berkeley CA, October 8, 2001.	
	5C	Julie WARD et al., Storage Area Network (SAN) Fabric Design, PowerPoint presentation given at the INFORMS International Conference, Maui HI, June 19, 2001.	
	5D	Julie WARD et al., Appia: Automatic Storage Area Network Fabric Design, Conference on File and Storage Technologies (FAST'02), pp. 203-217, January 28, 2002, Usenix, Berkeley, CA.	
	5E	Staffan Bo STRAND, Storage Area Networks and SANTK, Thesis, December 2001, University of Minnesota, Minneapolis, MN.	
	5F	Matthew T. O'KEEFE et al., Designing Fibre Channel Storage Area Networks, February 2001. < http://www.borg.umn.edu/fc/papers/SANTK.pdf >	
	5G	Robert E. BIXBY, Solving Real-World Linear Programs: A Decade and More of Progress, January 2002.	
	5H	Ravindra K. AHUJA et al., Network Flows: Theory, Algorithms, and Applications, pp. 4-9, 649-686, 1993, Prentice-Hall, Upper Saddle River, NJ.	
	5I	Christodoulos A. FLOUDAS et al., Quadratic Optimization, 1995.	
	5J	Parameswaran RAMANATHAN et al., Resource Placement with Multiple Adjacency Constraints in k-ary n-Cubes, 1995.	

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		Application Number	10/066,051		
		Filing Date	January 31, 2002		
		First Named Inventor	Li-Shiuan PEH		
		Art Unit	2123		
		Examiner Name	Kevin J. TESKA		
Sheet	6	of	6	Attorney Docket Number	100110288-1

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	6A	Staffan STRAND, Automatic Generation of Core/Edge Topology SANs Using SANTK, May 23, 2002.	
	6B	Joachim P. WALSER, Solving Linear Pseudo-Boolean Constraint Problems with Local Search, 1997, American Association for Artificial Intelligence, Menlo Park, CA.	
	6C	Julie WARD et al., Storage Area Network Fabric Design, PowerPoint presentation given at the Math Sciences Research Institute for Combinatorial Design, Berkeley CA, November 8, 2000.	

Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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